

Case No. 02AG38953426

**COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450**

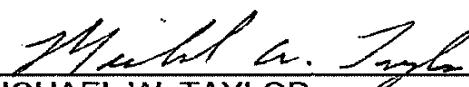
In re Application of: **BORGATTI ET AL.**
Serial No.: **10/768,401**
Confirmation No.: **3793**
Filed: **JANUARY 30, 2004**
For: **RECONFIGURABLE SIGNAL PROCESSING IC WITH
AN EMBEDDED FLASH MEMORY DEVICE**

Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

1. This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
2. This IDS is submitted under 37 C.F.R. § 1.97(c). The fee in the amount of \$180.00 is being paid by credit card on EFS-Web.
3. This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
4. This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$130.00 to cover the petition fee.
5. The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
6. Please associate this application with Customer No. **27975**.

Date: June 22, 2006


MICHAEL W. TAYLOR
Reg. No. 43,182

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
BORGATTI ET AL.)
)
Serial No. 10/768,401)
Confirmation No. 3793)
Filing Date: JANUARY 30, 2004)
)
For: RECONFIGURABLE SIGNAL)
 PROCESSING IC WITH AN EMBEDDED)
 FLASH MEMORY DEVICE)
)

Examiner: E. COLEMAN

Art Unit: 2183

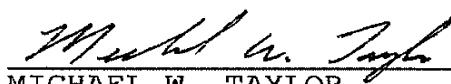
CITATION UNDER 37 CFR §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. In accordance with current USPTO procedures published 05 AUG 2003, in 1276 OG 55, copies of the U.S. patent documents cited in the form 1449A are not attached. The undersigned would be happy to provide copies of these references if requested. Copies of non-U.S. patent documents, if any, are attached. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

Respectfully submitted,


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SUBSTITUTE FORM PTO-1449A LIST OF PATENTS AND APPLICANT'S INFORMATION DISCLOSURE STATEMENT			Atty Docket: Serial No.: Applicant: Filing Date: Group:	02AG38953426 10/768,401 Borgatti et al. January 30, 2004				
U.S. PATENT DOCUMENTS								
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date	
	AA	5,693,570	12/2/97	Cernea et al.	437	205		
	AB	6,467,009	10/15/02	Winegarden et al.	710	305		
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub Class	Translation	
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)								
	AQ	David et al., DART: A Dynamically Reconfigurable Architecture Dealing with Future Mobile Telecommunications Constraints, Parallel and Distributed Processing Symposium, April 15, 2002, Pages 156-163						
	AR	XC6200 Field programmable Gate Arrays Product Description, April 24, 1997, Page 31						
	AS							
EXAMINER:				DATE CONSIDERED:				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant</p>								